Calling Applications for Full-Time Research Assistant (RA)

Grant Ref: MRG-2025 #6 | **Project Title:** *Improving the Quality and Readability of Sri Lankan Stone Inscriptions for Epigraphy*

Key Information	
Salary: LKR 60,000/ month (All-inclusive)	**Duration:** 7 Months
Application Deadline: 30th October 2026 (12:00 Noon)	**Interview Date:** 3rd November 2026

Project Scope & Responsibilities

- **Field Data Collection:** Responsible for conducting on-site data collection and sellipi image acquisition across selected historical locations on a fully self-directed basis.
- **Model Development:** Design, train, and evaluate Deep Learning (DL) and Machine Learning (ML) models for character recognition and classification of damaged Early Brahmi letters.
- **System Integration & Deployment:** Integrate core algorithms into a deployable software tool (web or mobile) to deliver a practical, workable solution for epigraphical research and public reference.
- **Validation & Dissemination:** Conduct field testing on historical stone inscriptions with domain epigraphers and assist the PI in technical documentation and journal manuscript drafting.

Minimum Qualifications & Skills

- **Education:** 4-year Bachelor's Degree (B.Sc. Eng. / B.Sc. Hons.) with a First Class or Second Class Upper in Electrical/Electronic Engineering, Computer Engineering, or Computer Science.
- **Technical Expertise:** Strong background in Signal Processing, Pattern Analysis and practical experience with Machine/Deep Learning frameworks.
- **Research Focus:** Demonstrated interest/track record in computer vision research, image analysis, and academic publication writing.

Application Procedure

Submit an application packet containing your **CV, Degree Certificate(s), Academic Transcripts, Publications, and a Cover Letter** detailing relevant experience via email to **Prof. Maheshi Dissanayake** (maheshid@eng.pdn.ac.lk) on or before **12:00 noon on 30th October 2026**.

Contact Information of Principle Investigator (PI)

Prof. Maheshi Dissanayake, Department of Electrical and Electronic Engineering, Faculty of Engineering, University of Peradeniya

15-Oct-2026
maheshid@eng.pdn.ac.lk